

Jc78 U.S. PTO  
09/621028  
07/21/00

	Subclass
Class	
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

Erg O.I.P.E.  
SCANNED AA Q.A. CS 2

PATENT DATE

APPLICATION NO. 09/621028	CONT/PRIOR D	CLASS 134	SUBCLASS 003	ART UNIT 1746	EXAMINER EL-ARWI
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APPLICANTS  
Eric Bergman

TITLE  
Process and apparatus for treating a workpiece such as a semiconductor wafer

PTO-2040  
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
<b>INTERNATIONAL CLASSIFICATION</b>					
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. 6,240,932	(Primary Examiner) _____ (Date)			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date)			Amount Due Date Paid	
ISSUE BATCH NUMBER					

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